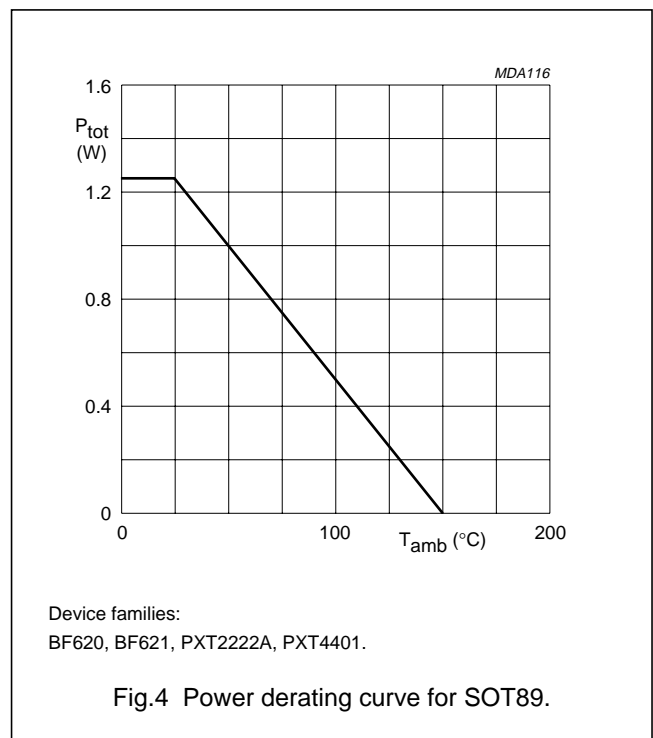
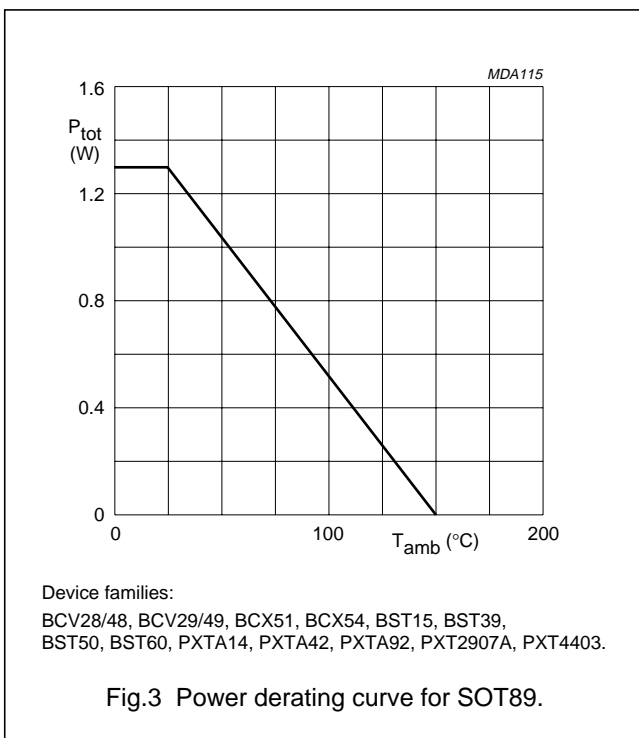
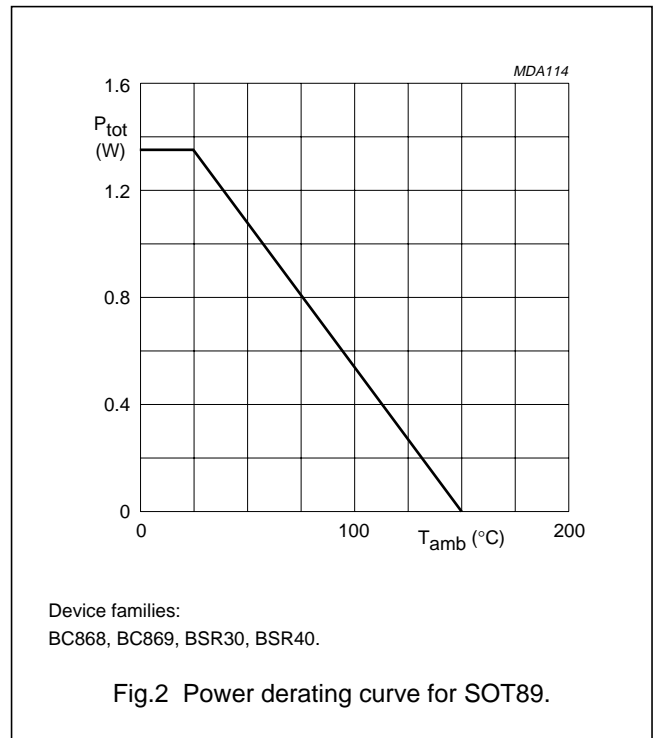
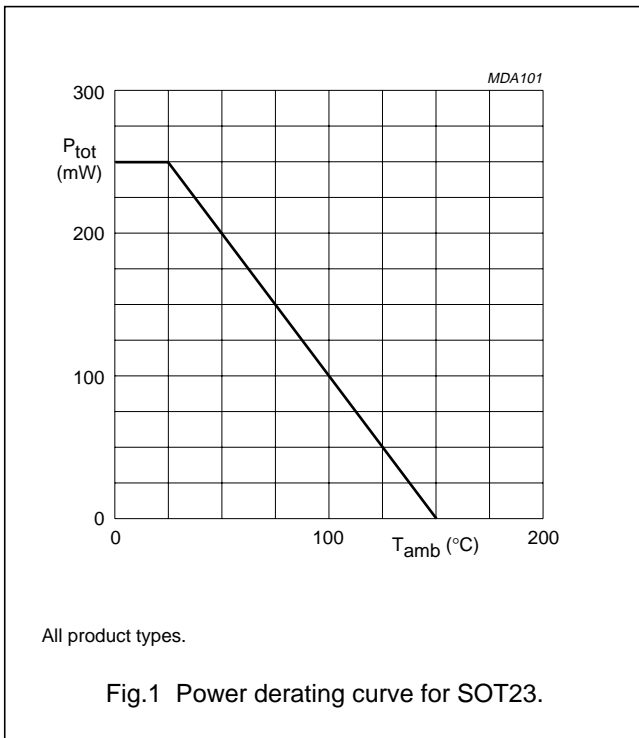


Power Derating Curves for SMDs

General

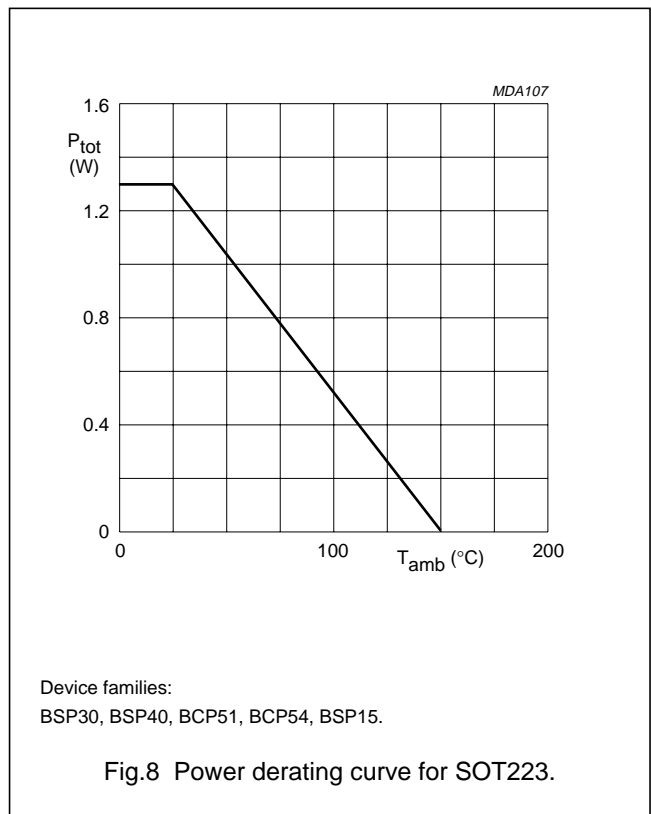
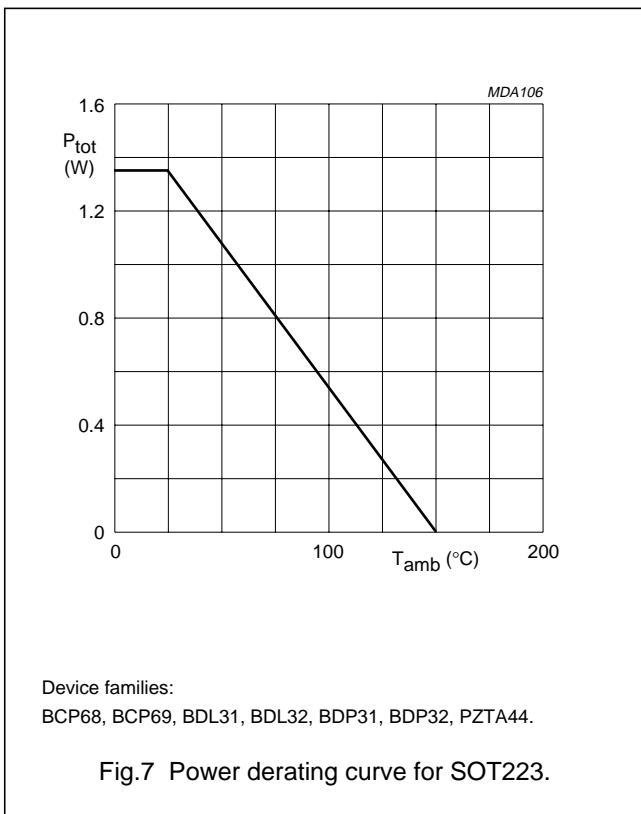
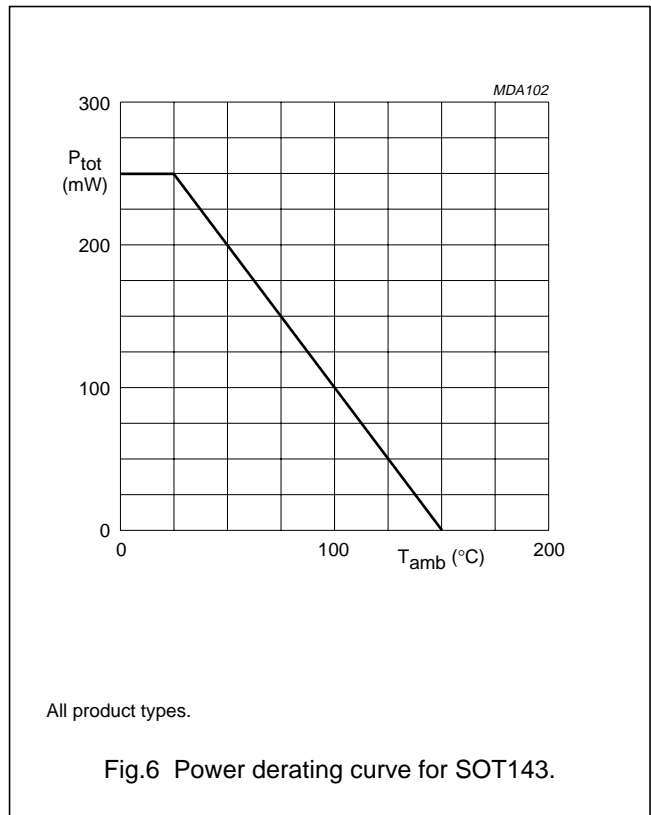
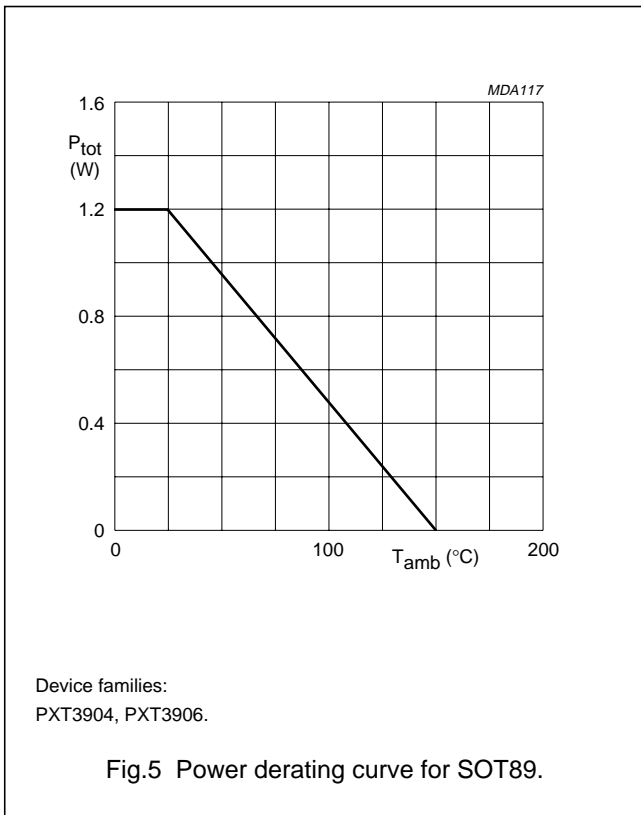
POWER DERATING CURVES FOR SMDs

Figures 1 through 17 on the following pages show the power derating curves (P_{tot} versus T_{amb}) for transistors in SMD packages.



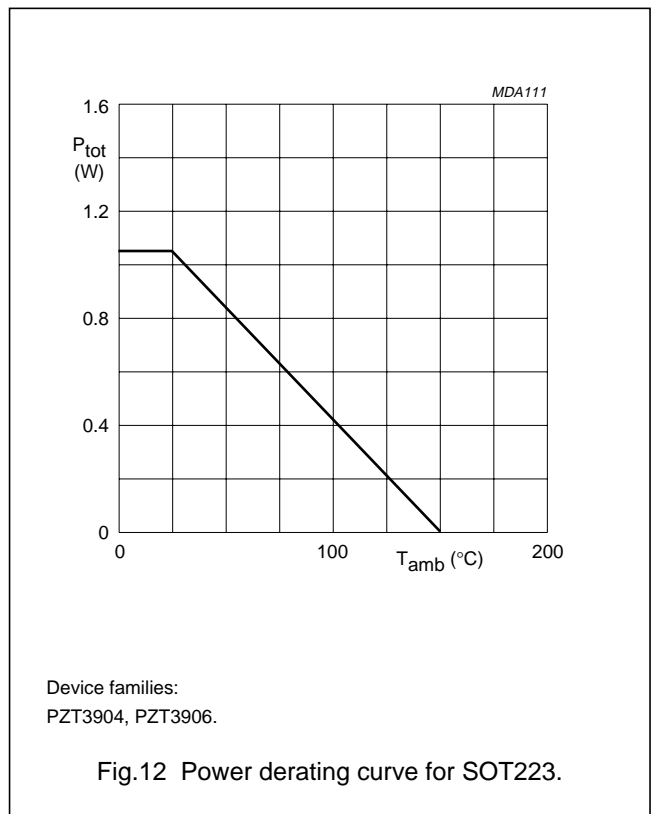
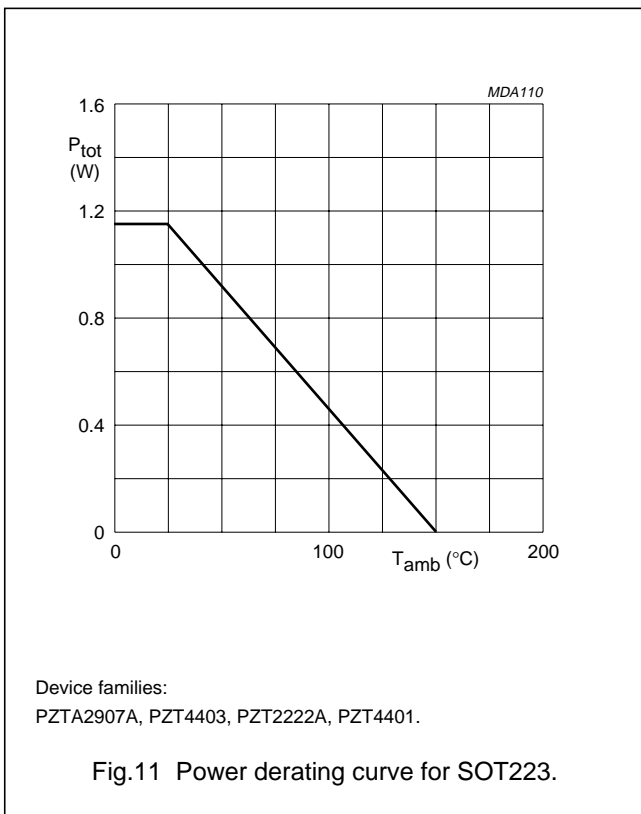
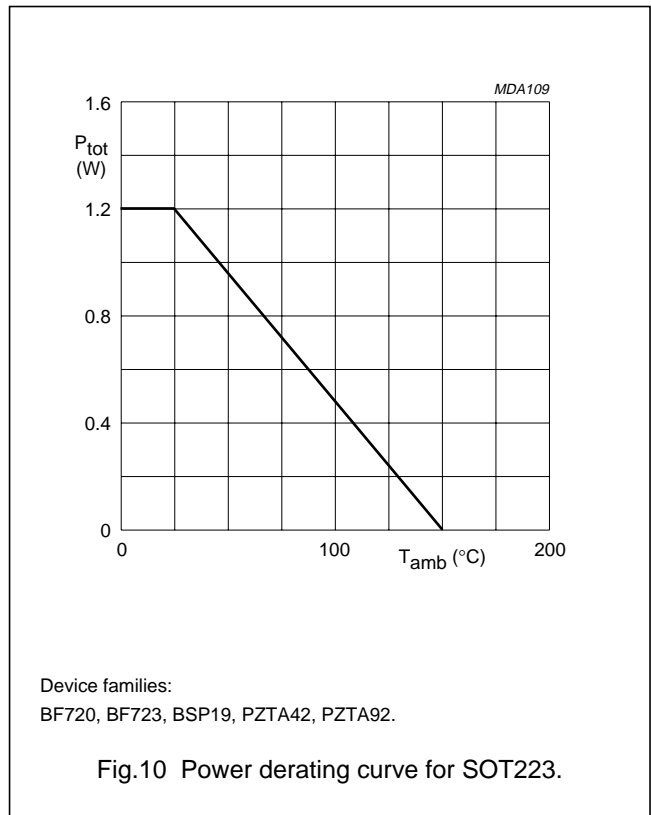
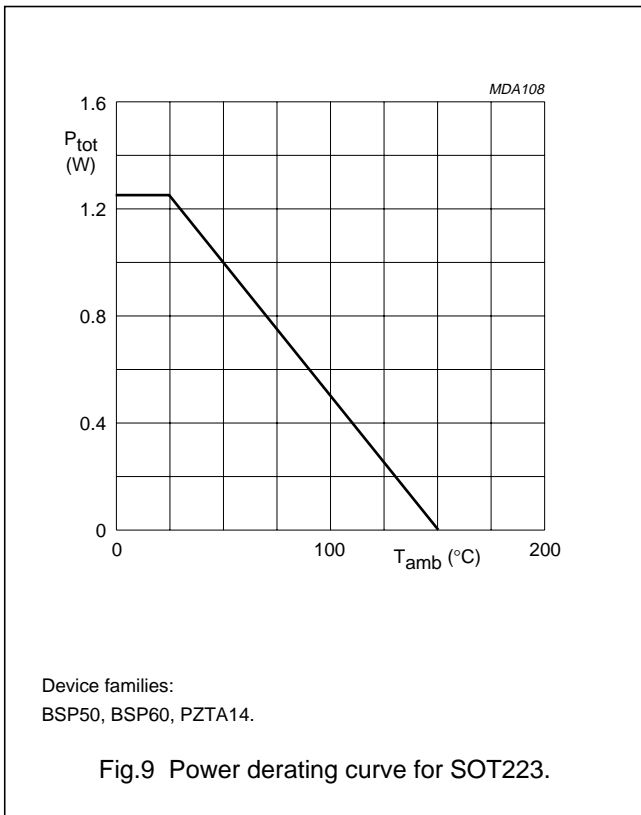
Power Derating Curves for SMDs

General



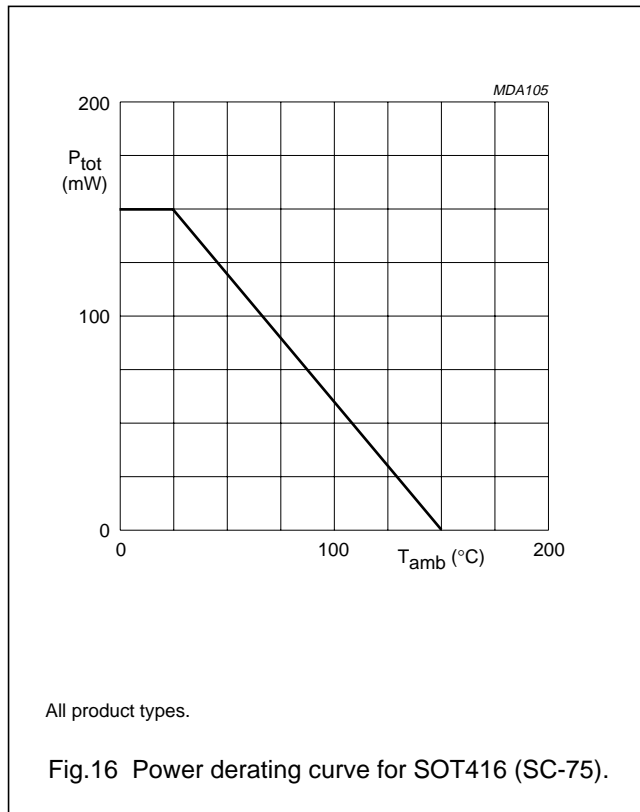
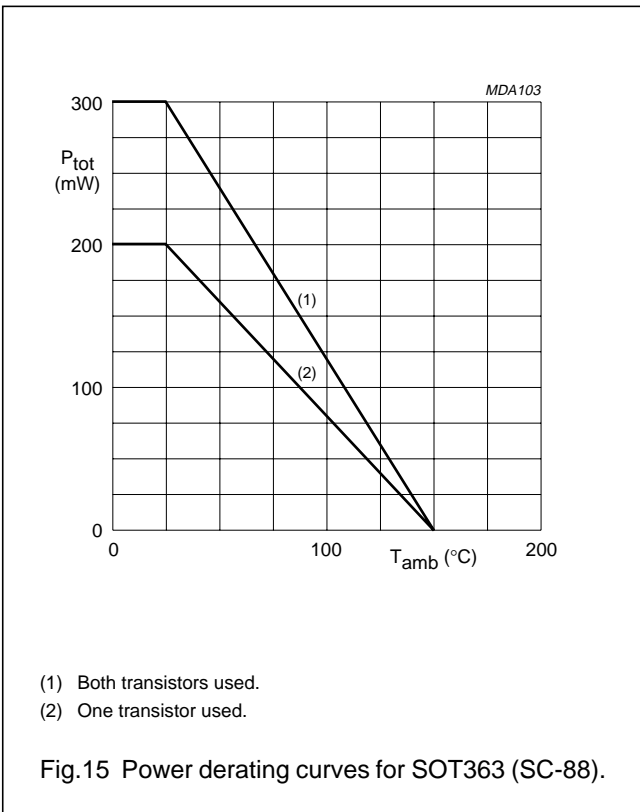
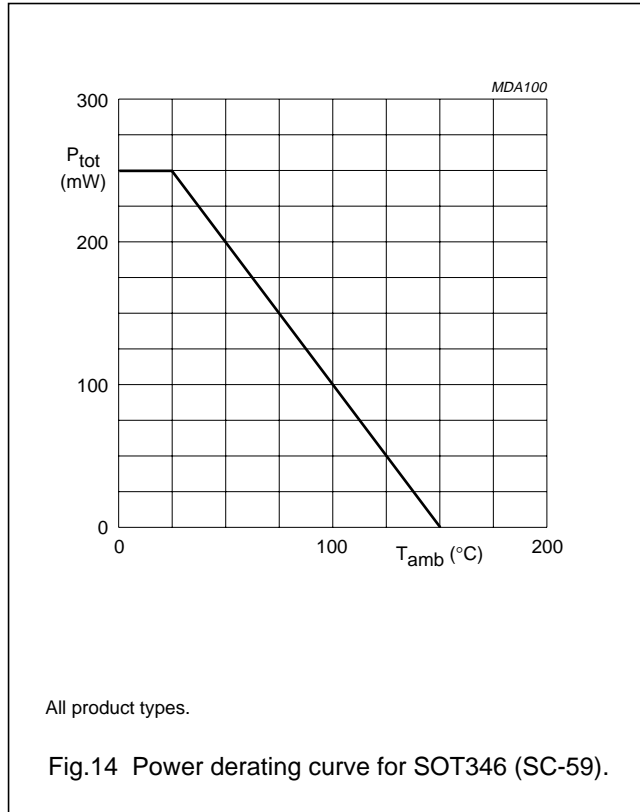
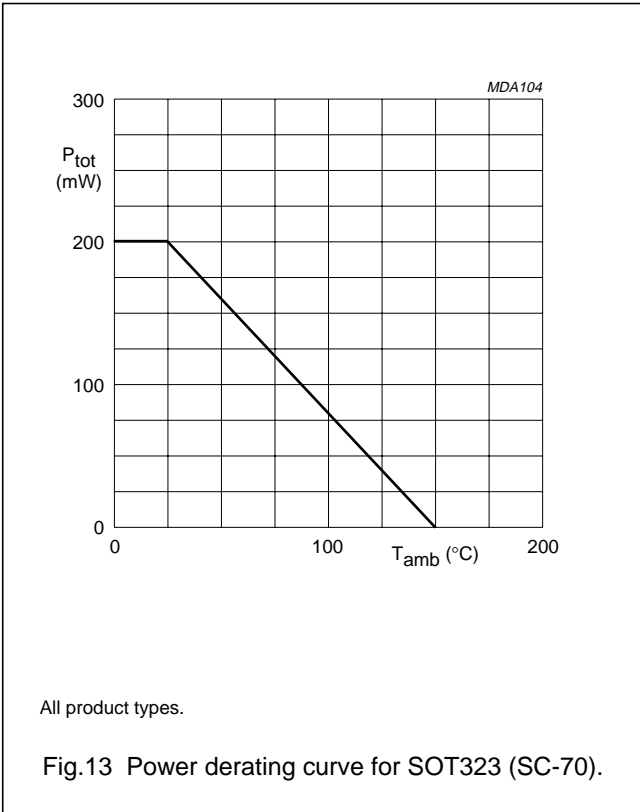
Power Derating Curves for SMDs

General



Power Derating Curves for SMDs

General



Power Derating Curves for SMDs

General

